Docket No.: M4065.0570/P570-A

Allowed: December 30, 2008

PATENT)

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Howard Rhodes et al..

Application No.: 10/660,565 Group Art Unit: 2811

Filed: September 12, 2003 Examiner: Andrew Owens Arena

For: METHOD OF FORMING CMOS IMAGER

PIXELS WITH CAPACITOR STRUCTURES

(AS AMENDED)

REQUEST FOR CORRECTED NOTICE OF ALLOWANCE AND REQUEST FOR ACKNOWLEDGEMENT OF REFERENCES CITED

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

Dear Sir:

In reviewing the above-captioned application file upon allowance, the undersigned has noticed that the title of invention as reflected on the Notice of Allowance and Fee(s) Due is incorrect.

The correct title of the invention should read as:

METHOD OF FORMING CMOS IMAGER PIXELS WITH CAPACITOR STRUCTURES

The proposed title was changed in Applicant's Amendment in Response to Non-Final Office Action dated August 16, 2006.

Applicants' undersigned representatives respectfully request that the Examiner acknowledge the references cited on Form PTO/SB/08A/B originally filed on March 13, 2006 with the Information Disclosure Statement. A copy of the original submission along with a copy of the PTO stamped postcard receipt, are being filed herewith for convenience.

Dated: February 2, 2009

Respectfully submitted,

Thomas J. D'Amico

Registration No.: 28,371

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Atty Docket No.: M4065.0570/P570-A

Inventor: Jeff A. Mckee

Application No.: 10/660,565-Conf. #5308 Filing Date: September 12, 2003 Title: METHOD OF FORMING CMOS IMAGER PIXEL DESIGNS (AS AMENDED)

**Documents Filed:** 

Information Disclosure Statement (3 pages)

Form PTO/SB/08A (1 page)

1 Article

Via:

Sender's Initials: TJD/GC/kmc

MAR 1 3 200 P

MAR 1 3 2006 to

Date: March 13, 2006

Docket No.: M4065.0570/P570-A

(PATENT)

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Jeff A. Mckee

Application No.: 10/660,565

Confirmation No.: 5308

Filed: September 12, 2003

Art Unit: 2822

For:

METHOD OF FORMING CMOS IMAGER

PIXEL DESIGNS (AS AMENDED)

Examiner: M. Lewis

## **INFORMATION DISCLOSURE STATEMENT (IDS)**

MS Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is directed to the references listed on the attached PTO/SB/08. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is filed more than three months after the U.S. filing date, OR more than three months after the date of entry of the national stage of a PCT application, AND after the mailing date of the first Office Action on the merits, whichever occurs first, but before the mailing date of a Final Office Action or Notice of Allowance (37 C.F.R. § 1.97(c)).

Application No.: 10/660,565 Docket No.: M4065.0570/P570-A

The attached reference was cited in an Office Action dated January 26, 2006 in a related application, serial number 11/165,940. I hereby certify that no item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application and, to my knowledge after making reasonable inquiry, no item of information contained in this Information Disclosure Statement was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this Information Disclosure Statement.

Those documents which are marked with a double asterisk (\*\*) next to the Cite No. in the attached form PTO/SB/08 are not supplied because they were previously cited by or submitted to the Office in a prior application number 11/165,490, filed June 24, 2005 and relied upon in this application for an earlier filing date under 35 U.S.C. § 120.

In accordance with 37 C.F.R. § 1.97(g), the filing of this Information

Disclosure Statement shall not be construed to mean that a search has been made. In accordance with 37 C.F.R. § 1.97(h), the filing of this Information Disclosure Statement shall not be construed to be an admission that any patent, publication or other information referred to therein is "prior art" for this invention unless specifically designated as such.

It is submitted that the Information Disclosure Statement is in compliance with 37 C.F.R. § 1.98 and the Examiner is respectfully requested to consider the listed references.

Application No.: 10/660,565 Docket No.: M4065.0570/P570-A

The Director is authorized to charge any deficiency in the fees filed, asserted to be filed or which should have been filed herewith (or with any paper hereafter filed in this application by this firm) to our Deposit Account No. 04-1073, under Order No. M4065.0570/P570-A. A duplicate copy of this paper is enclosed.

Dated: March 13, 2006

Respectfully submitted,

Thomas J. D'Amico

Registration No.: 28,371

Gabriela I. Coman

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PTO/SB/08a/b (07-05)
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Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

Sub	Substitute for form 1449A/B/PTO			Complete if Known		
				Application Number	10/660,565-Conf. #5308	
IN	<b>VEORMATION</b>	I DI	SCLOSURE	Filing Date	September 12, 2003	
STATEMENT BY APPLICANT				First Named Inventor	Jeff A. Mckee	
				Art Unit	2822	
	(Use as many sheets as necessary)			Examiner Name	M. Lewis	
Sheet	1	of	1	Attorney Docket Number	M4065.0570/P570-A	

U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No.1	Document Number  Number-Kind Code <sup>2</sup> ( if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No.1	Foreign Patent Document  Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> ( <i>if known</i> )	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Т <sup>6</sup>

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. ¹ Applicant's unique citation designation number (optional). ² See Kinds Codes of USPTO Patent Documents at <a href="https://www.uspto.gov">www.uspto.gov</a> or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁵ Applicant is to place a check mark here if English language Translation is attached.

NON PATENT LITERATURE DOCUMENTS					
Examiner Initials	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>		
		H. Watanabe et al., "Hemispherical Grained Si Formation on <i>in-situ</i> Phosphorus Doped Amorphous-Si Electrode for 256Mb DRAM's Capacitor", IEEE Transactions on Electron Devices, Vol. 42, No. 7, July 1995.			

<sup>\*</sup>EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Examiner	Date	
Signature	Considered	

<sup>&</sup>lt;sup>1</sup>Applicant's unique citation designation number (optional). <sup>2</sup>Applicant is to place a check mark here if English language Translation is attached.